

A₁ concl.
must be stored on the network interface in an SA cache. Due to such limitations, the INTEL PRO/100 S Server Adapter, for example, supports only a limited number of connections that can use Inline Receive. Other connections use the Secondary Use model to offload secure traffic, though Secondary Use adds latency to packets at several steps. The primary source of the increased latency for Secondary Use is the delay related to the final interrupt of the Secondary Use operation.

At page 8, please replace the third paragraph with the following:

A₂
Decryption engines process data at a rate of approximately 600 Megabits per second ("Mbit/sec"). The latency from the device Interrupt Request line ("IRQ") to interrupt processing is based on measurements on INTEL PENTIUM III Processor and INTEL PENTIUM 4 Processor systems using a MICROSOFT WINDOWS 2000 Operating System. Notably, the value of this latency does not change significantly with processor speed.

IN THE CLAIMS:

Please amend the claims as follows:

- A₃
11. (Amended) The computing system of claim 10, wherein said network driver parses said encrypted packet, matches said encrypted packet with one of said at least one SA and instructs said controller to transfer said encrypted packet and said one SA across said bus to said controller.
 - A₄
23. (Amended) The device of claim 22, wherein prior to the instructions to convert said encrypted packet, said system further includes instructions to: